

Constraining Designs For Synthesis And

word w e r o F It has been said that Ótiming is everything.Ó While that is certainly true if youÕre in show business, the same holds true if youÕre designing a system-on-a-chip (SoC).

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Over-constraining a design can result in a significant increase in the time required to place, route and analyze a design. The result is a longer design implementation time. Since the design implementation phase potentially occurs many times during a design cycle this can have a significant impact on design efficiency. A more serious design over-constraint consequence occurs when the place-and ...

Chapter 9 Design Constraints and Optimization

Timing constraints: are they constraining designs Qiuyang Wu has been in the EDA industry for 15 years, with expertise in STA, Synthesis, Timing

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